

08/22/03



Case No. 62603

DIRECTOR,  
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WASHINGTON, DC 20231

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PATENT APPLICATIONS, ASSISTANT  
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D.C. 20231.

22141 U.S. PRO  
10/647061



Transmitted herewith for filing is the patent  
application of:

EXPRESS MAIL NO: EV301515986US

DATE OF DEPOSIT: August 22, 2003

NAME: JUSTIN GOREE

SIGNATURE: *Justin Goree*

Inventor: **Robert J. MEARS**

For: **METHOD FOR MAKING SEMICONDUCTOR DEVICE INCLUDING  
BAND-ENGINEERED SUPERLATTICE**

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Patent Application: 35 pages, 76 claims.  
9 sheets of drawings.  
The suggested drawing figure to be published is FIG. 1.  
Applicant qualifies as a small entity under 37 CFR § 1.27.

Assignee info:  
Name: **MEARS CORPORATION**  
Address: **189 Wells Avenue, Suite 100, Newton, MA 02459**  
State of Incorporation: **Massachusetts**

☒ Applicant claims priority benefit to the following U.S. applications:

Application No.: \*\*\*\*\*  
Filing Date: **June 26, 2003**

Application No.: \*\*\*\*\*  
Filing Date: **June 26, 2003**

☒ The Declaration and Filing Fee are NOT ENCLOSED.

☒ Name, Address and Citizenship of Inventors are as follows:

**Robert J. MEARS**  
**14 Summit Road**  
**Wellesley, MA 02482**  
**Citizen of United Kingdom**

☒ PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN

☒ Associate this file with Customer No. 27975.



**27975**

PATENT TRADEMARK OFFICE

Date: August 22, 2003

*Christopher F. Regan*

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